ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

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Title of Invention	JITTER-RESISTIVE DELAY LOCK LOOP CIRCUIT FOR LOCKING DELAYED CLOCK AND METHOD THEREOF

Submision Type : Utility Patent Filing

Application Number:

10/711313

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First Named Applicant: Jui-Hsing Tseng

Attorney Docket Number: MTKP0186USA

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File Listing:

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